

1/12

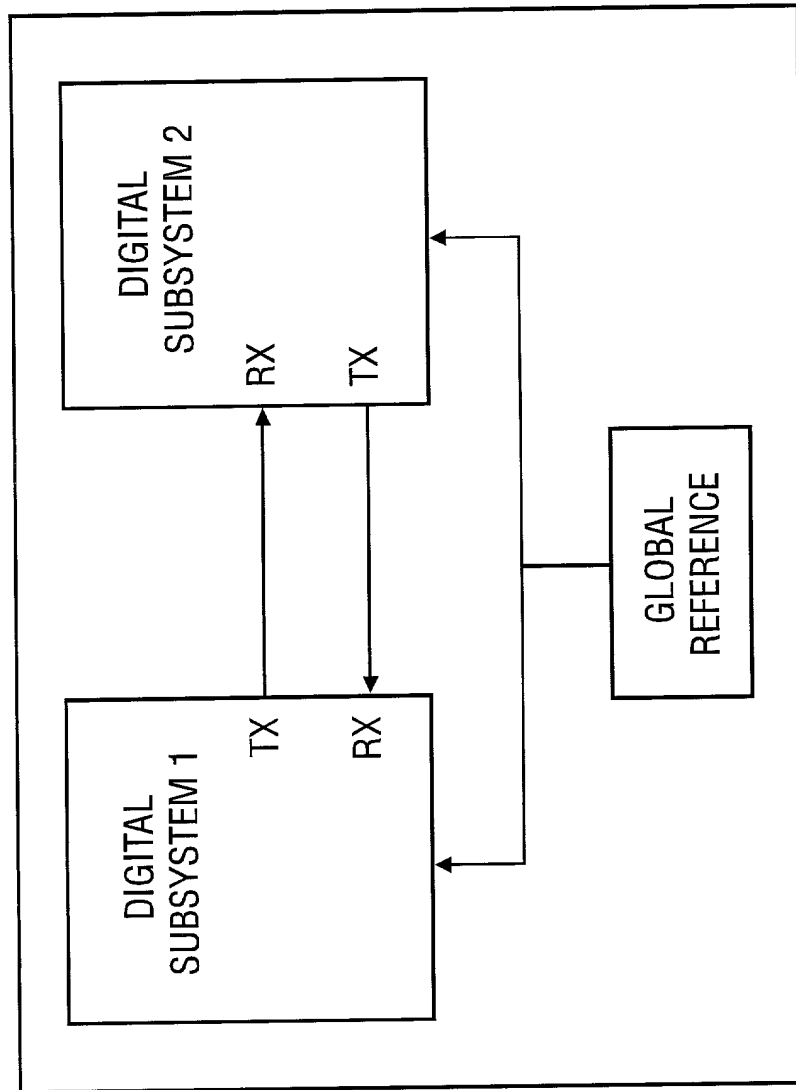
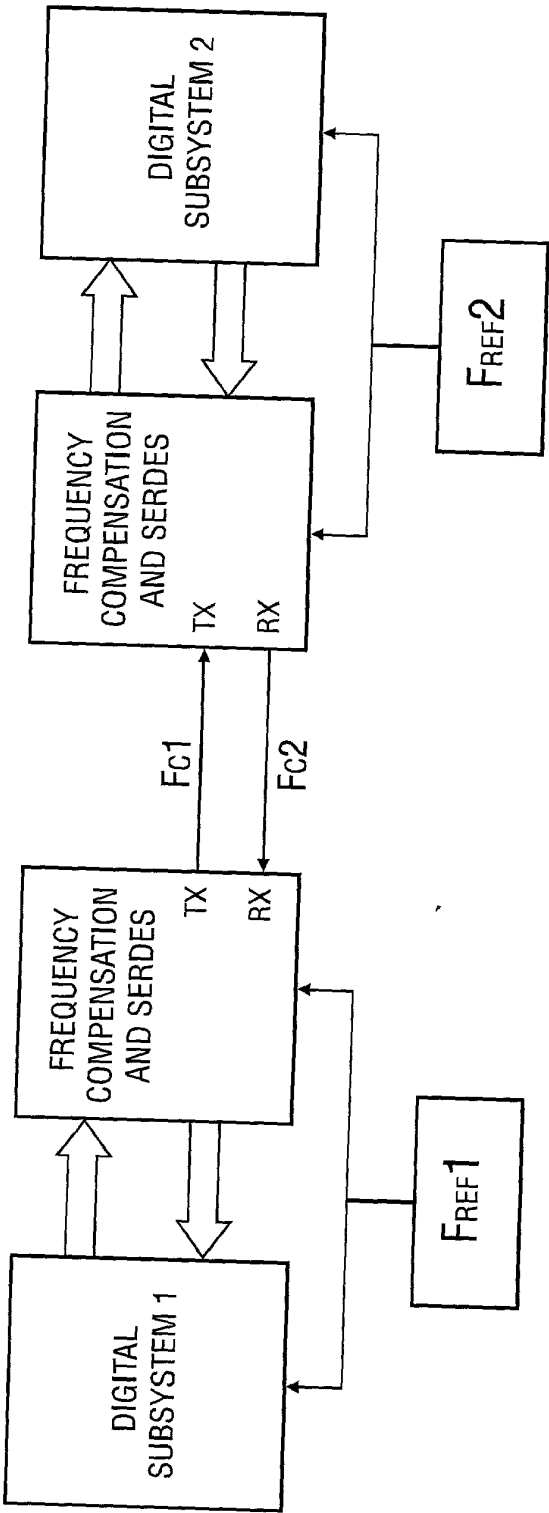


Fig. 1



CONVENTIONAL PLESIOCHRONOUS SYSTEM

Fig. 2

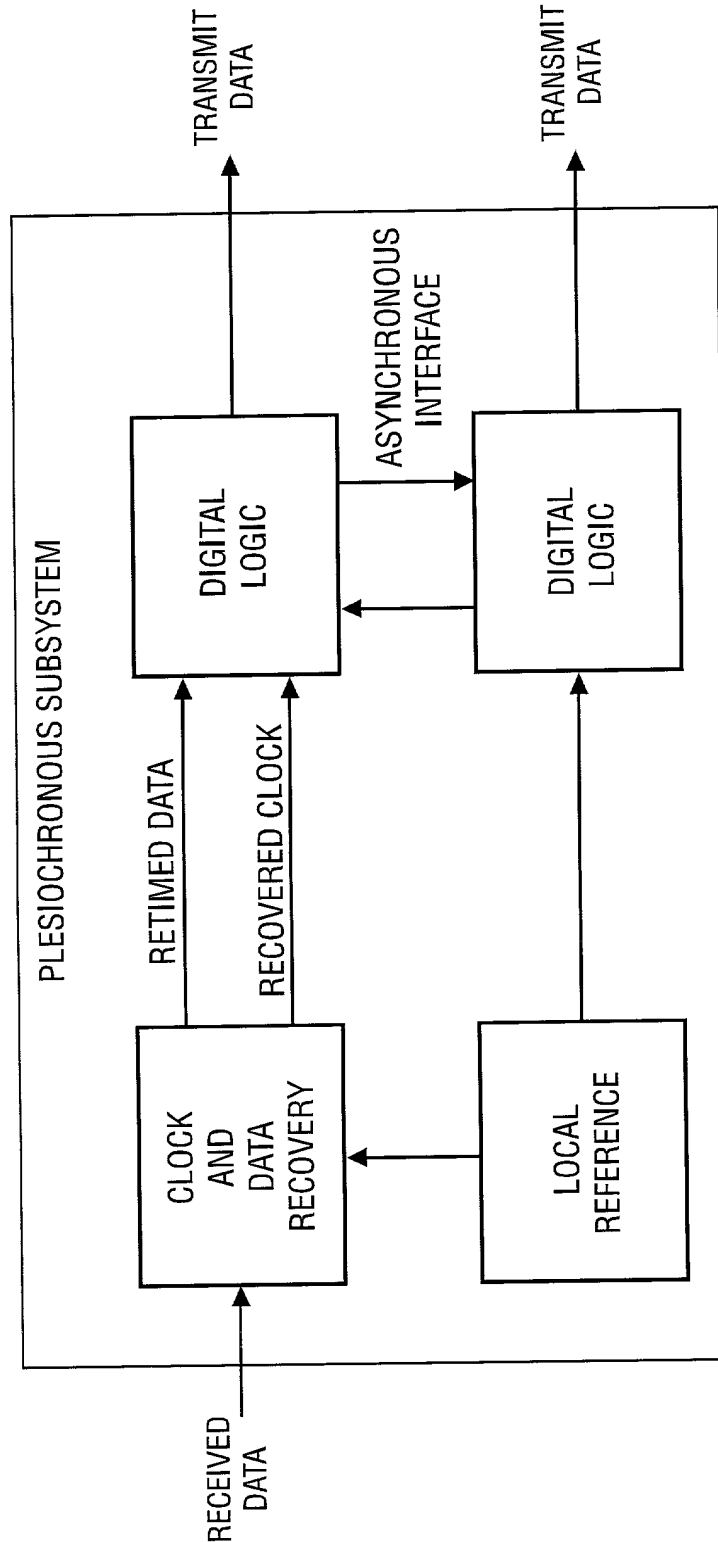
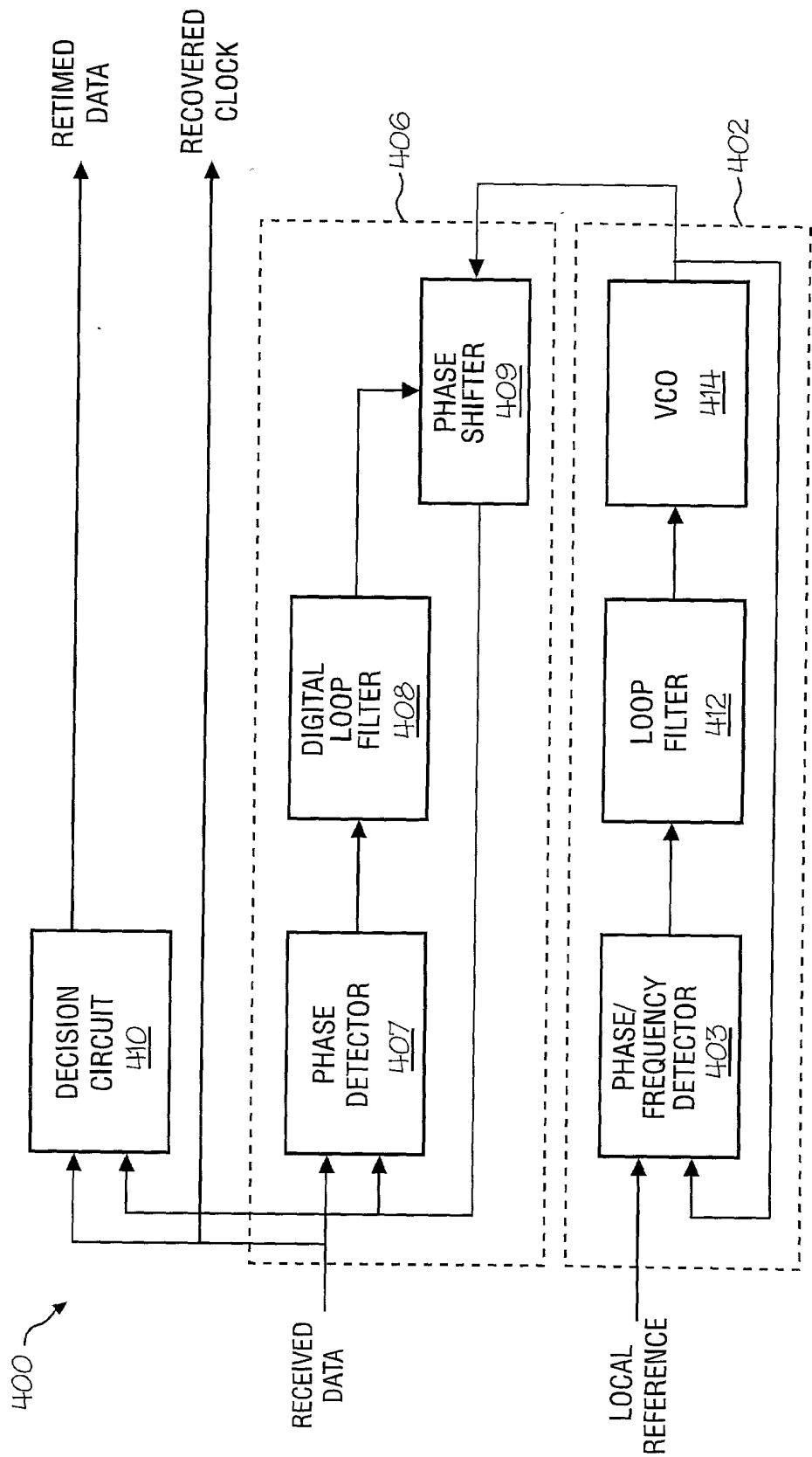


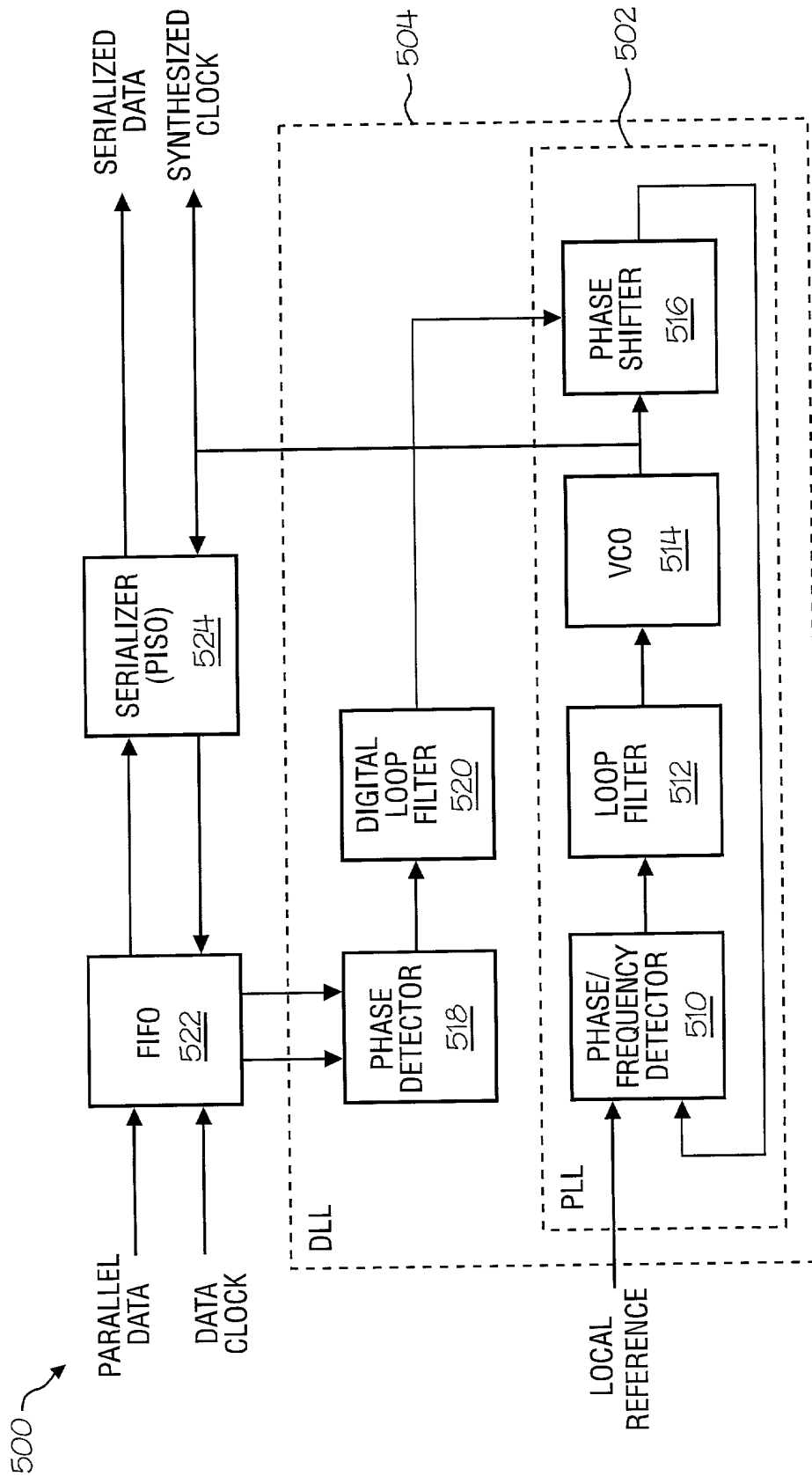
Fig. 3



DUAL LOOP CDR

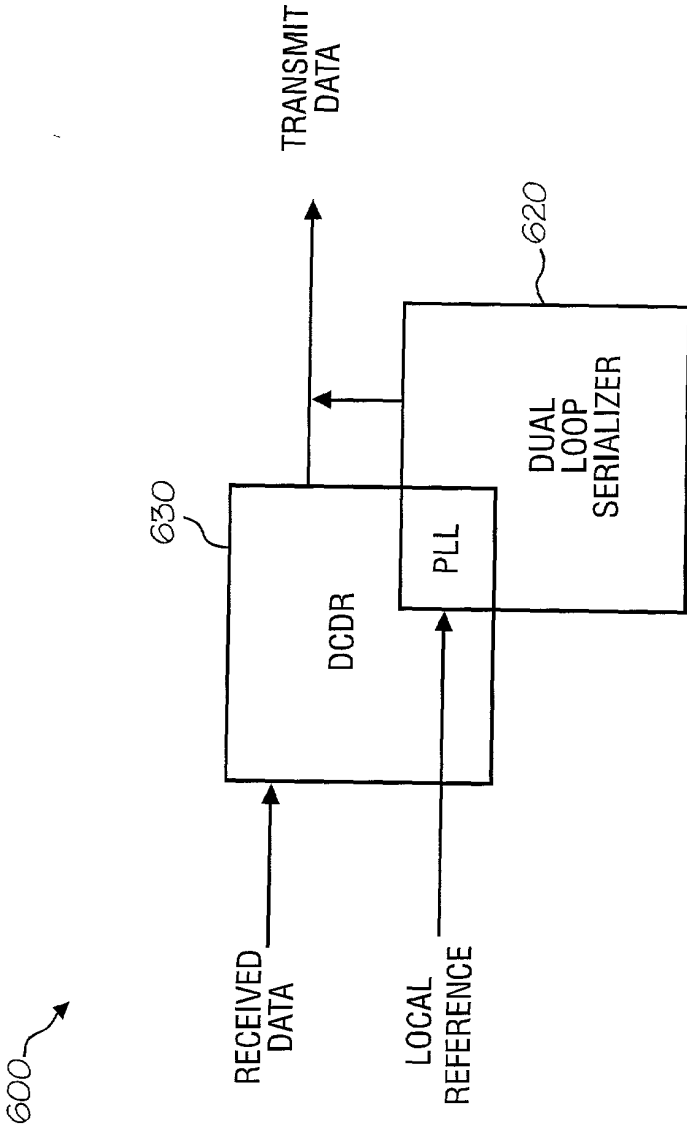
Fig. 4
PRIOR ART

5/12



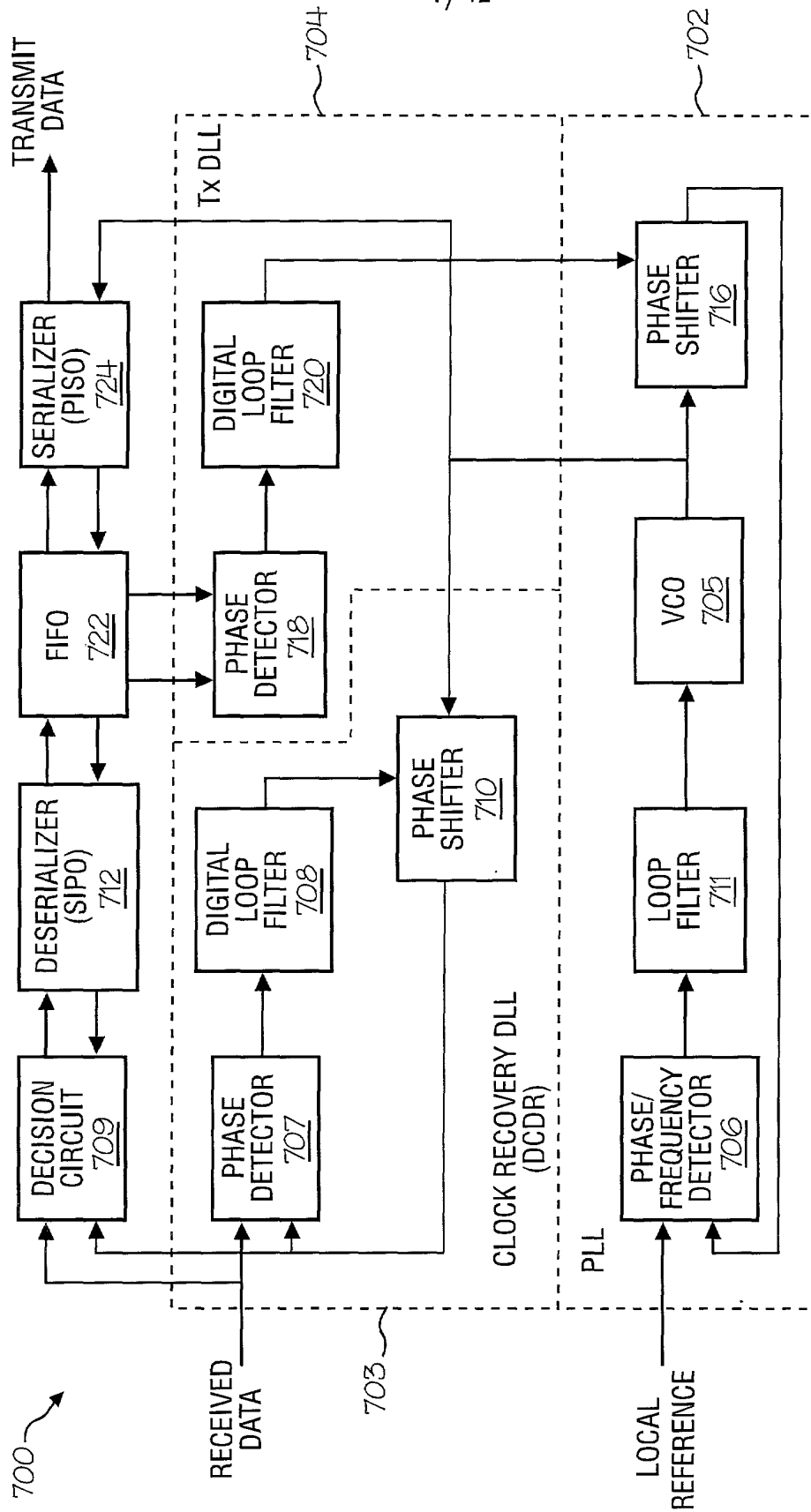
DUAL LOOP SERIALIZER

Fig. 5



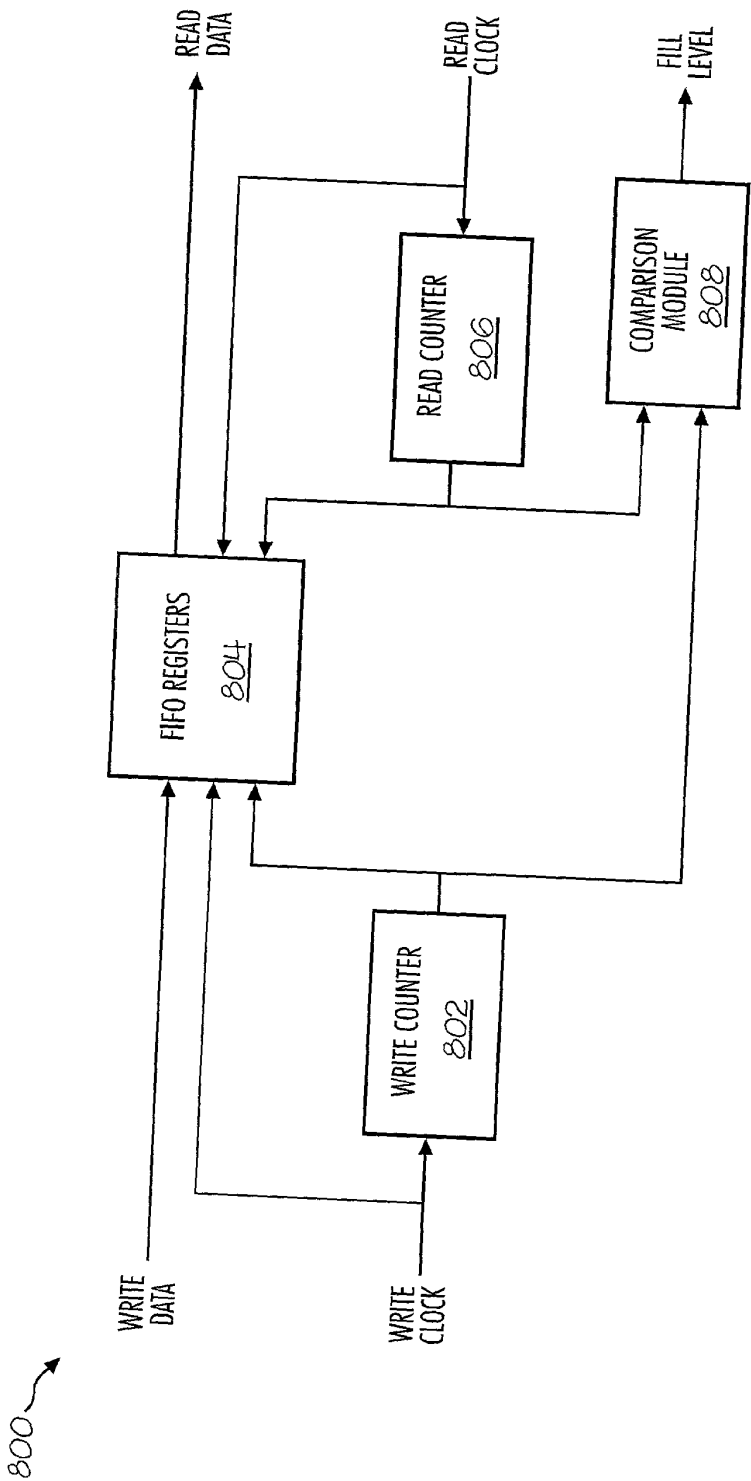
DUAL LOOP RETIMER
Fig. 6

7/12



DUAL LOOP RETIMER

Fig. 7



GRANULAR FIFO FILL LEVEL INDICATOR

Fig. 8

9/12

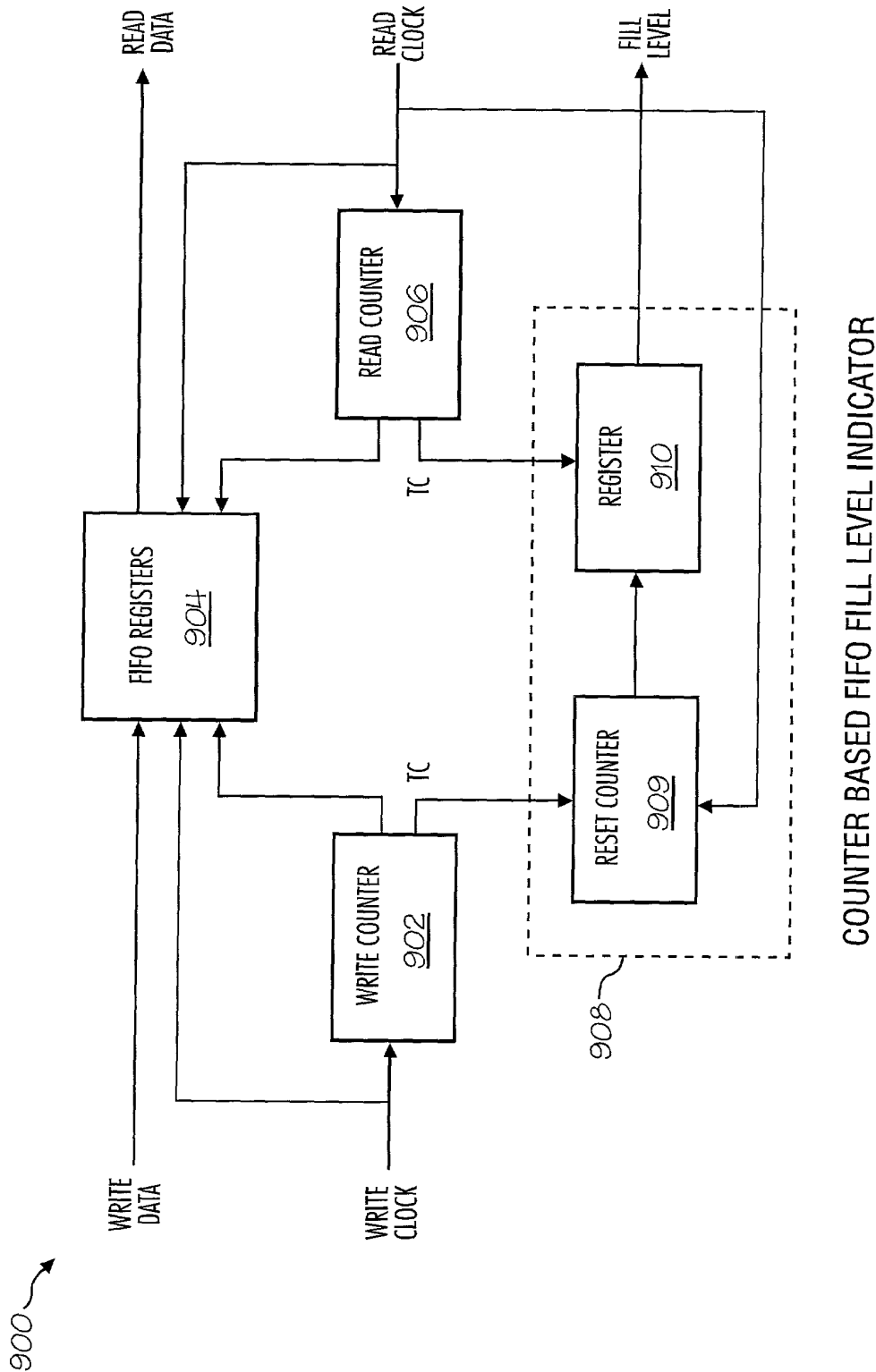
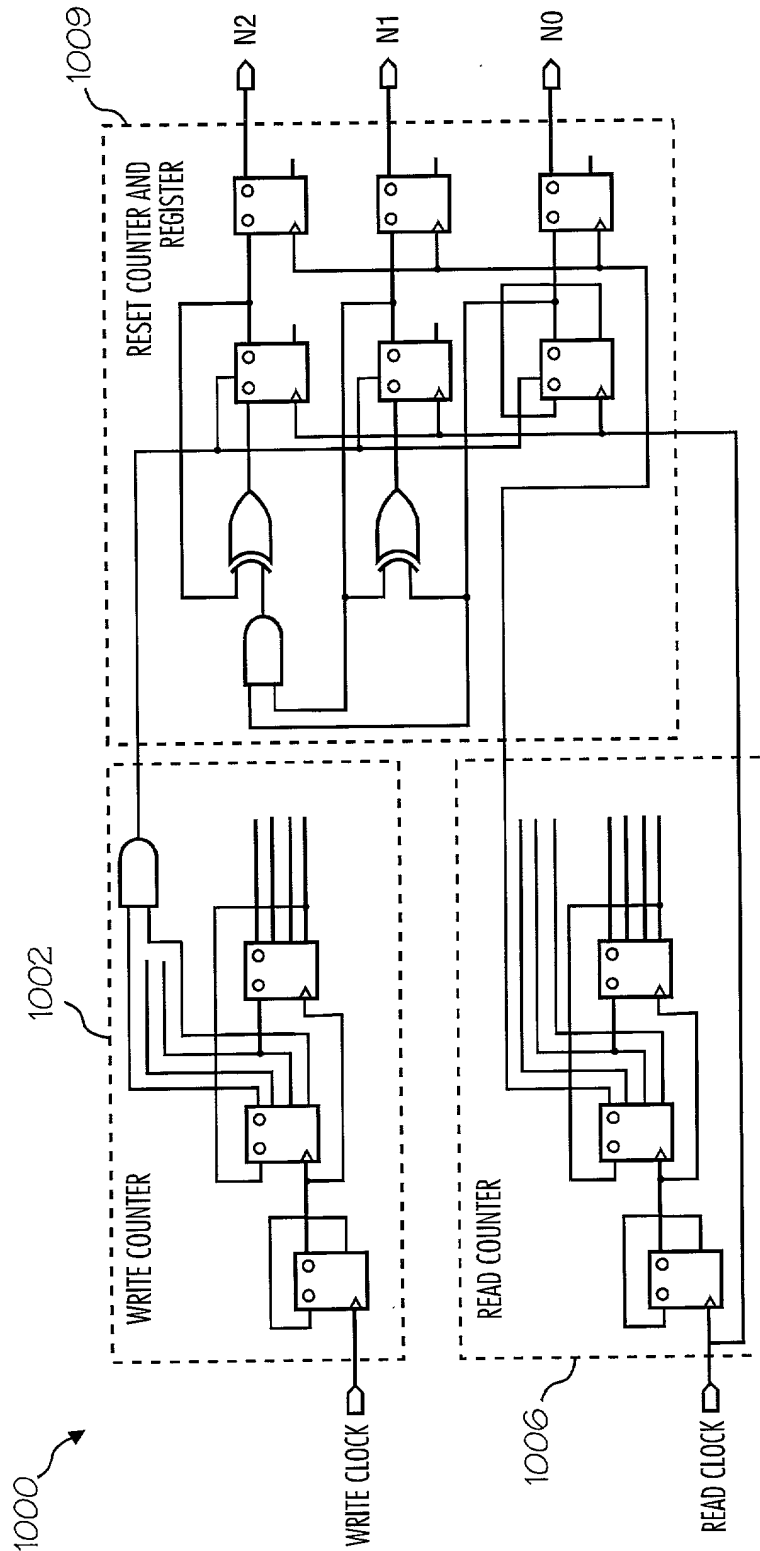


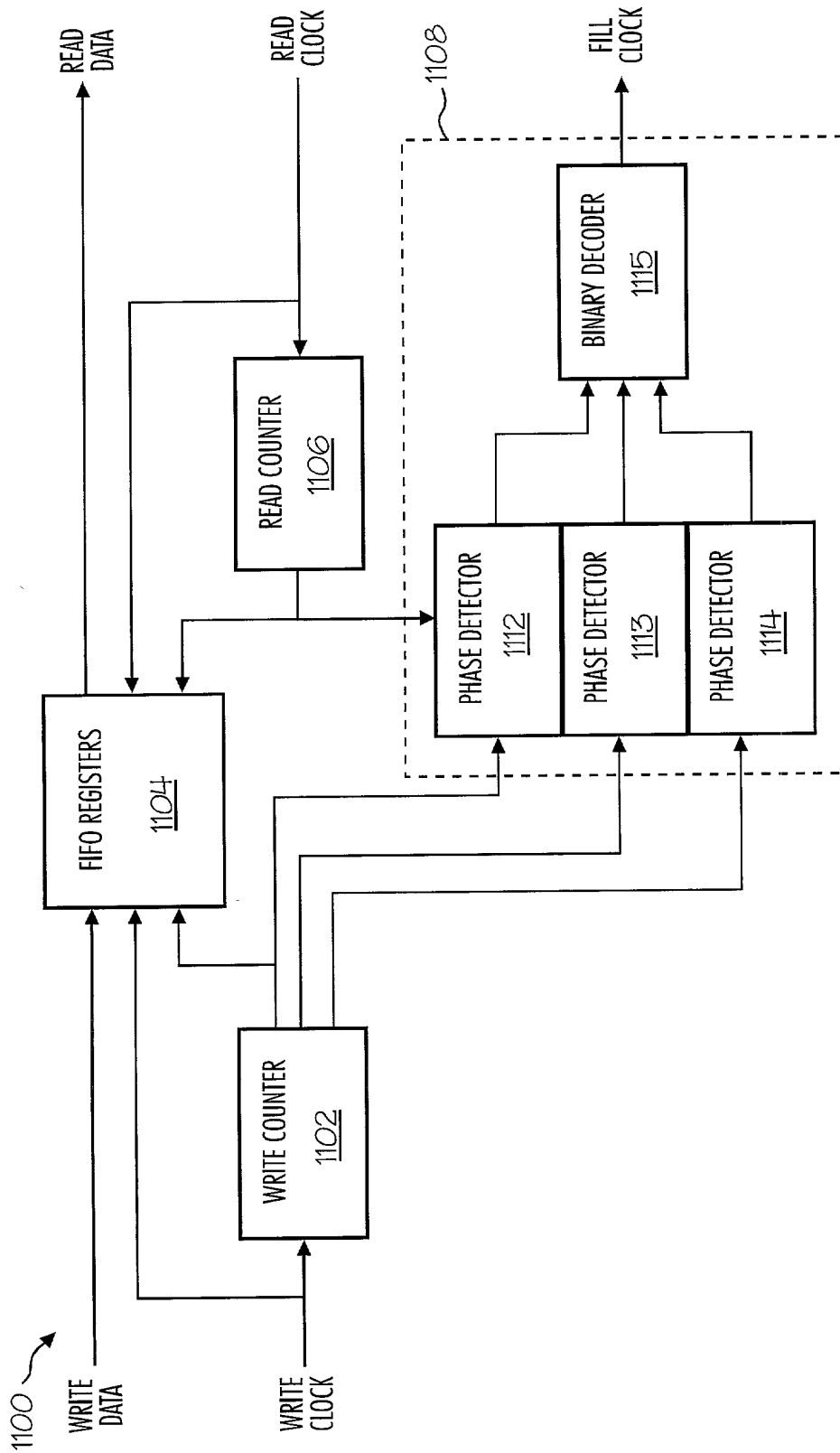
Fig. 9



LOGIC LEVEL IMPLEMENTATION OF SYSTEM 900

Fig. 10

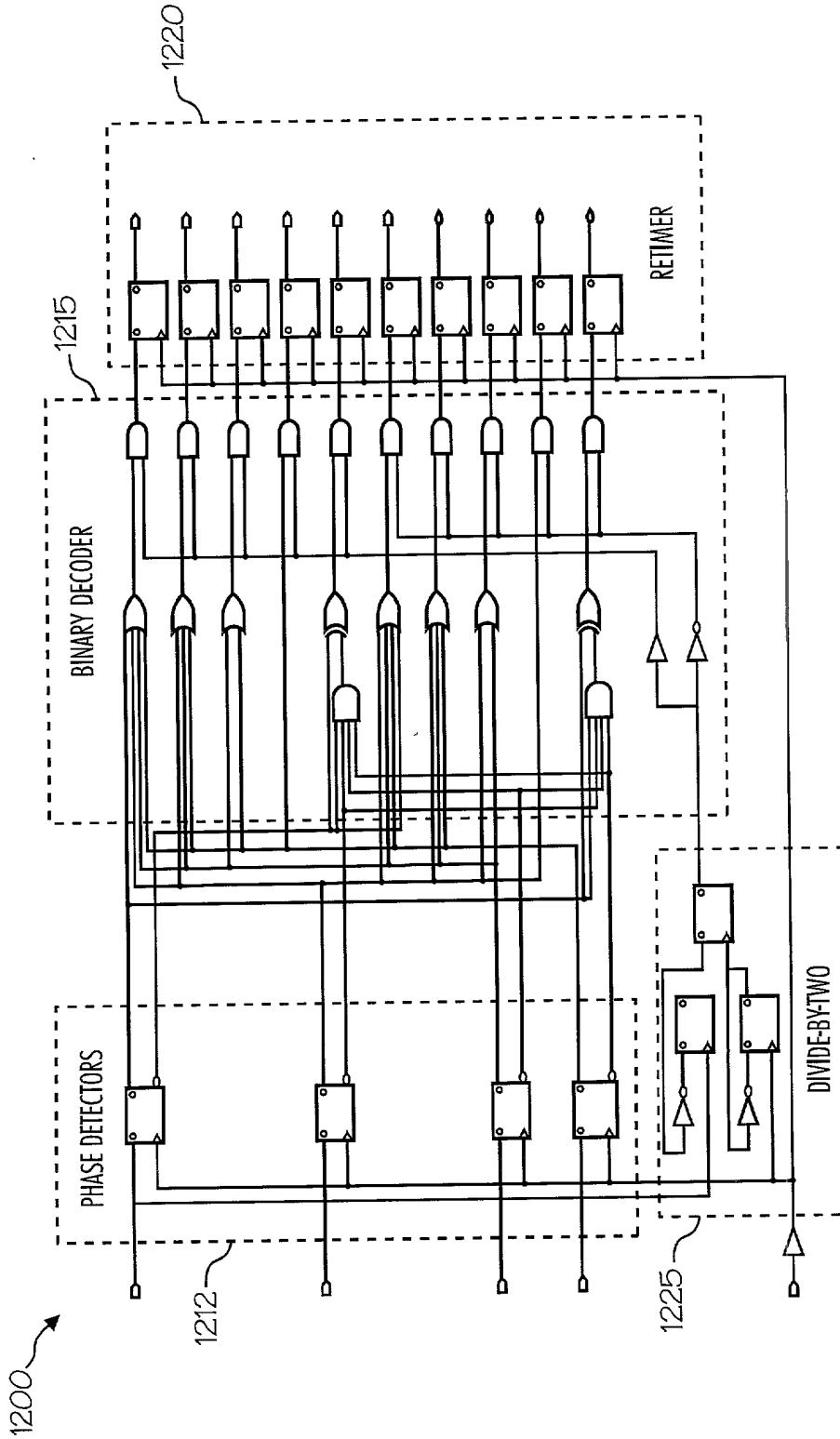
11/12



MULTI-PHASE DETECTOR BASED FIFO FILL LEVEL INDICATOR

Fig. 11

12/12



LOGIC LEVEL IMPLEMENTATION OF SYSTEM 1100

Fig. 12